FORM PTO-1449 (Modified)	ATTY. DOCKET NO. YOR9200010367	SERIAL NO.:09/847776
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	APPLICANT: Erik R. Altman et al	
(Use several sheets if necessary)	FILING DATE: May 2, 2001	GROUP:

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REFERENCE DESIGNATION — U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA						
	AB						
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	AQ	IEEE Computer March 2000 "Dynamic and Transparent Binary Translation" M. Gschwind et al pp. 54-59
	AR	ACM/IEEE 32nd Intl. Symposium on Microarchitecture, " Optimizations and Oracle Parallellism with Dynamic Translation" K. Ebcioglu et al
	AS	ACM/IEEE Computer Sigplan Notices Vol. 22, #7 July1987 "Mimic: A Fast System/370 Simulator" Cathy May

EXAMINER	DATE CONSIDERED

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REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA	6 2 0 2 1 3 2	03/13/01	Nayeem Islam et al	711	141	12/29/99
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO
	AL							
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	AN							
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	AQ		M. K. Gschwind, "Method and apparatus for determining branch addresses in programs generated by binary translation" Research Disclosure, Vol. 41, #416, article 41698, Dec. 1998
	AR		Computer IEEE 1993 "An Architectural Framework for Supporting Heterogeneous Instruction-Set Architectures" Gabriel M. Silberman et al pp 39-56, June 1993
	AS		

EXAMINER	DATE CONSIDERED

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